

# A Hybrid Statistical and Prioritised Unequal Error Protection Scheme for IEEE 802.11n LDPC Codes

Original Research Paper

## Tulsi Pawan Fowdur

Faculty of Engineering  
Department of Electrical and Electronic Engineering, University of Mauritius  
Reduit, Mauritius  
p.fowdur@uom.ac.mu

## Madhavsingh Indoonundon

Faculty of Engineering  
Department of Electrical and Electronic Engineering, University of Mauritius  
Reduit, Mauritius  
madhavsingh.indoonundon@uom.ac.mu

**Abstract** –The combination of powerful error correcting codes such as Low Density Parity Check (LDPC) codes and Quadrature Amplitude Modulation (QAM) has been widely deployed in wireless communication standards such as the IEEE 802.11n and DVB-T2. Recently, several Unequal Error Protection schemes which exploit non-uniform degree distribution of bit nodes in irregular LDPC codes have been proposed. In parallel, schemes that exploit the inherent UEP characteristics of the QAM constellation have also been developed. In this paper, a hybrid UEP scheme is proposed for LDPC codes with QAM. The scheme uses statistical distribution of source symbols to map the systematic bits of the LDPC encoded symbols to the QAM constellation. Essentially, systematic symbols having highest probabilities of occurrence are mapped onto the low power region of the QAM constellation and those with a low probability of occurrence are mapped onto the high power region. The decrease in overall transmission power allows for an increased spacing between the QAM constellation points. Additionally, the scheme uses the distribution of the bit node degree of the LDPC code-word to map the parity bits having the highest degree onto prioritised QAM constellation points. Simulations with the IEEE 802.11n LDPC codes revealed that the proposed scheme can provide gains of up to 0.91 dB in  $E_b/N_0$  compared with other UEP schemes for a range of Bit Error Rate (BER) values.

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**Keywords** – IEEE 802.11n, Low-Density-Parity-Check codes, Quadrature Amplitude Modulation, Unequal Error Protection

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## 1. INTRODUCTION

In 1962, LDPC codes, which are a class of linear block codes, were invented by Gallager [1]. After that, in 1996, David Mackay re-introduced LDPC codes [2] and in 1998 he came up with Non-Binary LDPC codes that outperformed LDPC codes [3]. Since LDPC codes can achieve near Shannon limit performances [4], they are considered to be one of the most powerful classes of error correcting codes developed to date. Consequently, several communications standards such as WiMax [5], DVB-T2 [6] and IEEE 802.11n [7] have adopted LDPC codes. The 802.11n standard combines QAM with LDPC codes and uses several code lengths ranging from 648 to 1944, with code rates of 1/2, 2/3, 3/4 and 5/6 [7], [8]. Recently, several papers have shown that the use of UEP can provide significant performance gains with LDPC codes and other coding schemes that are combined with QAM. An overview of UEP schemes developed for LDPC codes as well as schemes which exploit the UEP potential of QAM is given next.

The QAM constellation has an interesting characteristic that allows UEP to be performed as demonstrated by the bit-reordering scheme proposed in [9]. The authors [9] combined LTE Turbo codes with QAM and UEP was used to provide greater protection to the systematic bits. Consequently, significant performance gains were obtained [9]. In [10], the authors extended the work of [9] with joint source channel decoding for LTE Turbo codes. The same UEP principle was applied to IEEE 802.11n LDPC codes along with a modified hybrid ARQ scheme in [11]. An interesting scheme in [12] performed UEP by mapping more important bits of an image to variable nodes with higher degrees in irregular LDPC codes. After LDPC encoding the systematic bits were mapped onto a power efficient QAM constellation and the parity check bits onto a spectrally efficient 16-QAM constellation. Significant performance gains were obtained with the proposed scheme [12]. Additionally, in a non-binary LDPC coded modulation system, the authors [13] employed an UEP scheme based on the principle of

bit reliability. At the BER of  $10^{-5}$ , the scheme achieved a gain between 0.1 and 0.5 dB [13]. Furthermore, in [14], structured rate-compatible codes with UEP were designed. The codes for the source-relay and source destination were optimized together. A significant gain was obtained over conventional LDPC codes and punctured rate compatible LDPC codes [14]. Finally, in [15], a statistical QAM based modulation scheme for low complexity video transmission was proposed. The rationale behind the scheme was to map the most frequent pixel values onto the QAM constellation points with the lowest energy. Consequently, the average energy needed for image transmission was much smaller and allowed for increasing the spacing between QAM constellation points for the same average energy thus improving the BER [15].

In this paper, a hybrid UEP scheme for binary LDPC codes with QAM is proposed. In this scheme, the source is assumed to be a set of variable-length coded characters with unequal probability distribution. The scheme then uses the statistical distribution of the source symbols to map the systematic bits of the LDPC encoded symbols to the QAM constellation. Essentially, systematic symbols having highest probabilities of occurrence are mapped onto the low power region of the QAM constellation as in [15], and those with low probabilities of occurrence are mapped onto the high power region. Consequently, the spacing between constellation points can be increased for the same average energy, hence reducing the overall power required for transmission. Additionally, the scheme uses the distribution of bit node degrees of the LDPC code to map the parity bits having the highest degrees onto prioritised QAM constellation points [12]. In general, the proposed hybrid UEP scheme combines the concepts of statistical QAM (S-QAM), prioritised constellation mapping and uneven degree distribution of bit nodes with binary LDPC codes. Simulations are performed with the IEEE 802.11n LDPC codes parity check matrices. Results showed that the proposed hybrid scheme can provide gains ranging from 0.23 dB to 0.91 dB in  $E_b/N_0$  for code rates of 1/2, 2/3 and 3/4 compared with other UEP schemes for a range of Bit Error Rate (BER) values. The contribution of this work is a new UEP scheme which consists of hybridization of performance-enhancing schemes such as UEP and S-QAM.

This paper is structured as follows. Section 2 gives the transmitter and receiver system models with the hybrid UEP scheme. Section 3 presents simulation results and analysis. Section 4 concludes the paper.

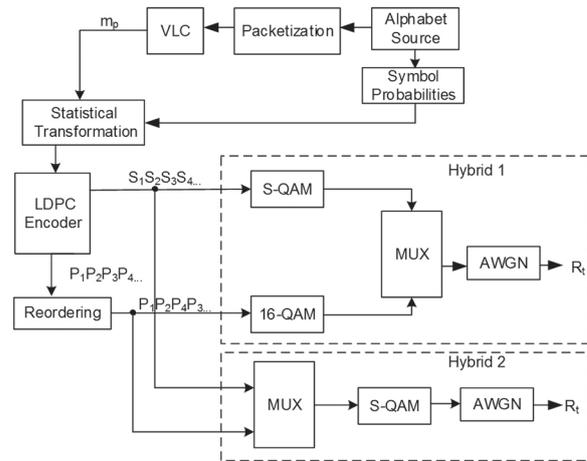
## 2. TRANSMITTER AND RECEIVER SYSTEMS FOR HYBRID SCHEME

### 2.1 TRANSMITTER

The input data is a random alphabet source with unequiprobable probability distribution. The letters of the alphabet present the alphabet source, and their corresponding probabilities are given in Table 1. Once the alphabets have been generated, variable-length cod-

ing (VLC) is performed to assign code-words, as given in Table 1, thereto.

The block diagram of the transmitter is shown in Fig.1.



**Fig. 1.** Transmitter with statistical QAM transformation and bit reordering

**Table 1.** The alphabet source and their VLC code-words.

Alphabet	Probability	Code-word
a	0.4	00
b	0.25	10
c	0.2	11
d	0.1	011
e	0.05	010

Let the code-word length of the LDPC code be  $L$ . If the code-rate is  $R$ , the number of systematic bits in the code-word,  $N_s$ , is given as  $N_s = R \times L$ .

The alphabet stream is packetised by adding as many characters in the packet such that the number of bits obtained after performing variable-length coding on the characters in the packet is equal to the number of systematic bits,  $N_s$ . If the number of bits obtained is less than that required for LDPC encoding, redundant bits are added at the end of the packet.

In this paper, two versions of a hybrid scheme with S-QAM are proposed. The first scheme (Hybrid 1) is described now.

Statistical transformation is performed on the VLC bit stream of each packet,  $m_p$ . With S-QAM (Statistical QAM) [15], the a-priori probabilities of the QAM symbols are derived by first obtaining the a-priori probabilities of systematic bit 1 and 0 from Table 1 by using the law of total probability as follows:

$$\begin{aligned}
 p(0) &= p(0|a) \times p(a) + p(0|b) \times p(b) + p(0|c) \times p(c) + \\
 &\quad p(0|d) \times p(d) + p(0|e) \times p(e) \\
 &= (1)(0.4) + (1/2)(0.25) + (0)(0.2) \\
 &\quad + (1/3)(0.1) + (2/3)(0.05) \\
 &= 0.5917
 \end{aligned} \tag{1}$$

$$\begin{aligned}
p(1) &= p(1|a) \times p(a) + p(1|b) \times p(b) + p(1|c) \times p(c) \\
&\quad + p(1|d) \times p(d) + p(1|e) \times p(e) \\
&= (0)(0.4) + (1/2)(0.25) + (1)(0.2) \\
&\quad + (2/3)(0.1) + (1/3)(0.05) \\
&= 0.4083
\end{aligned} \tag{2}$$

When 16 QAM is employed ( $M=16$ ), the message bit stream  $m_p$  is grouped into symbols of 4 ( $\log_2 M$ ) bits (0000, 0001, ..., 1111). The a-priori probability of each of these symbols is given as:

$$\begin{aligned}
&p(\text{symbols}_{(m_b)}) \\
&= \prod (\text{probability of each bit in the symbol}) \tag{3}
\end{aligned}$$

For example,

$$p(0000) = p(0) \times p(0) \times p(0) \times p(0) = 0.1226.$$

The probabilities for the whole set of symbols are given in Table 2.

After computing the probability of each message symbols, they are sent to the statistical transformation block to generate the S-QAM transformation lookup table. This block transforms the most probable symbols obtained from  $m_p$  to the lowest power symbols and the least probable ones into the highest power symbols.

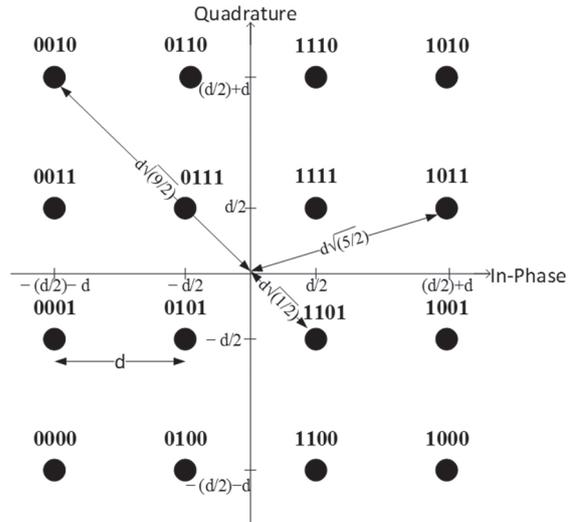
Table 2 is the statistical transformation lookup table used if 16 QAM is employed in our case:

**Table 2.** S-QAM transformation lookup table.

Initial symbol	Probability	Transformed symbol
0000	0.1226	1111
0001	0.0846	0001
0010	0.0846	0101
0011	0.0584	0011
0100	0.0846	1101
0101	0.0584	0100
0110	0.0584	0110
0111	0.0403	1110
1000	0.0846	0111
1001	0.0584	1001
1010	0.0584	1011
1011	0.0403	0000
1100	0.0584	1100
1101	0.0403	0010
1110	0.0403	1000
1111	0.0278	1010

To understand the rationale behind this transformation, consider the conventional 16-QAM constellation given in Fig. 2. It is observed that 0000, which is a symbol with a high probability of occurrence, is mapped onto a high power point, whereas 1111, which is a symbol of low probability, is mapped onto a low power point. Hence the bits in every symbol being 0000 obtained from  $m_p$  are flipped so that these symbols become 1111. In a similar way, some bits in every symbol

being 0100 (a high probability symbol) are flipped to transform the symbol to 1101 (a low power symbol). These transformations help to reduce the overall transmission power.



**Fig. 2.** 16-QAM constellation diagram.

Note that the QAM constellation point positions are not interchanged so as to preserve the UEP characteristics of the constellation. Instead, bits are flipped in a structured way in every symbol to reduce the transmission power.

LDPC encoding of the statistically transformed message obtained at the output of the Statistical Transformation block is then performed. An LDPC encoded code-word  $c$  for a binary message  $u$  can be obtained using the generator matrix  $G$  in the following matrix equation [16], [17]:

$$c = u \cdot G, \tag{4}$$

where,

$u$  is a row vector with the message bits,

$C$  is the encoded code-word,

$G$  is the generator matrix.

In this paper, the generator matrix used is as per the IEEE 802.11n LDPC codes [7].

The parity bits  $P_1, P_2, \dots, P_N$  obtained from LDPC encoding are reordered so that they can benefit from UEP due to a prioritised mapping in the QAM constellation. The reordering is performed based on the degree distribution of parity bit nodes. The degree of a node is defined as the total number of edges linked to the node as illustrated by the Tanner graph [12]. A bit node degree is also equal to the number of 1s in the column of the parity check matrix corresponding to that bit node.

A section of the Tanner graph for the IEEE 802.11n parity check matrix with  $L=648$  and rate =  $1/2$  is given in Fig. 3 and it is clearly observed that not all bit nodes have the same degree. For example, bit nodes 350 and 351 have degree 3, while bit node 352 has degree 2.



the parity bits are modulated using the conventional Gray-coded QAM.

For QAM, the average power is given by:

$$P_{avg} = \sum_{i=0}^{M-1} (d_i^2 \times p_i), \quad (5)$$

where,

$d_i$  is the distance from the symbol  $i$  to the origin in the constellation diagram,

$p_i$  is the probability of QAM symbol  $i$ ,

$M$  is the QAM order.

Each distance  $d_i$  can be expressed in terms of the separation between the neighbouring symbols,  $d$ , as shown for a 16 QAM constellation in Fig. 2.

With the probabilities from Table 2,  $P_{avg}$  is calculated for the standard QAM approach using the conventional constellation point separation distance  $d=2$  ( $P_{avg1}$ ).  $P_{avg}$  is then evaluated for the S-QAM approach, where more probable QAM symbols are mapped at lower power points, in terms of an unknown constellation distance  $d=ds$  ( $P_{avg2}$ ). To achieve the same power transmission for both constellations,  $P_{avg1} = P_{avg2}$ . This equation is solved to obtain  $ds$ , which will be larger than the conventional constellation point separation distance [15].

For example, using the QAM symbol probabilities from Table 2,  $P_{avg1} = 11.46$  W. After applying statistical transformation as in Table 2, whereby higher probability symbols are altered in order to have lower power,  $P_{avg2} = 2.045 d_s^2$ . Solving  $P_{avg1} = P_{avg2}$ ,  $d_s = 2.37$ . Hence for 16-QAM,  $d_s = 2.37$ .

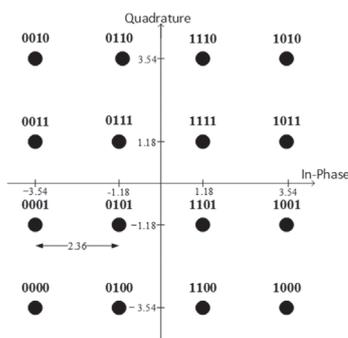
Table 3 contains the values of  $d_s$  obtained for 16 and 64 QAM for the Hybrid 1 scheme.

**Table 3.** Calculated values of  $ds$  for different cases.

QAM order (M)	$ds$
16	2.37
64	2.59

The constellation points for the S-QAM scheme are then obtained using the value of  $d_s$  and the increased spacing between them improves the BER performance.

For example, the constellation diagram for implementing S-QAM on LDPC codes with  $M=16$  is shown in Fig.5.



**Fig. 5.** The S-QAM constellation diagram for LDPC code with  $M=16$ .

In the Hybrid 2 scheme shown in Fig.1, the parity bits are multiplexed with systematic bits and modulated onto the S-QAM constellation instead of the conventional 16-QAM. This method allows parity bits to benefit from the increased constellation point spacing in the S-QAM constellation for improving the overall performance. Hence it is necessary to use the probability of QAM symbols obtained from the whole code-word to calculate the appropriate separation distance between the S-QAM constellation points. The a-priori probability of each QAM symbol obtained from parity bits is assumed to be equal to 1/16 (equiprobable symbols). Thus the a-priori probability of each QAM symbol obtained from the whole code-word is calculated as follows:

$$p(\text{Symbol}_{\text{codeword}}) = p(\text{Symbol}_{\text{mb}}) \times R + p(\text{Symbol}_{\text{parity}}) \times (1 - R), \quad (6)$$

where  $R$  is the LDPC code-rate given by the ratio of the number of systematic bits to the total number of bits in the code-word.

For example, if  $R=1/2$ ,

$$\begin{aligned} p(0000\text{message}) &= 0.1226 \\ p(0000\text{parity}) &= 1/16 \\ p(0000\text{code-word}) &= 0.1226 \times (1/2) \\ &+ (1/16) \times (1-1/2) = 0.09255 \end{aligned}$$

The probabilities of the remaining symbols are obtained in a similar way.

Using the same method as for the Hybrid 1 scheme but with the probabilities of the QAM symbols obtained from the whole code-word, the values of  $d_s$  are calculated for different LDPC code-rates and QAM modulation orders. Table 4 contains the values of  $d_s$  obtained for 16 and 64 QAM with different code-rates for the Hybrid 2 scheme.

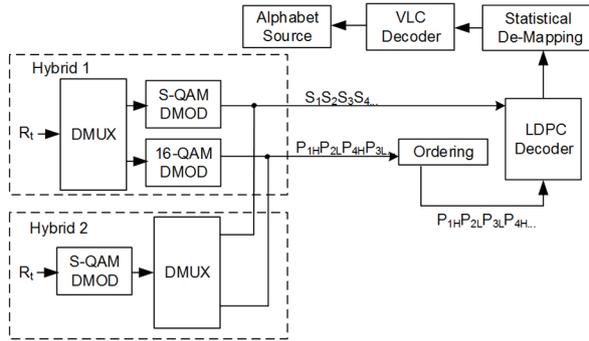
**Table 4:** Calculated values of  $d_s$  for different cases using the Hybrid 2 scheme.

QAM order (M)	Code-Rate(R)	$ds$
16	1/2	2.17
16	2/3	2.23
16	3/4	2.26
64	1/2	2.31
64	2/3	2.40
64	3/4	2.44

## 2.2 RECEIVER

For the Hybrid 1 scheme, the received QAM symbols,  $R_r$  are first de-multiplexed into systematic and parity symbols. Systematic symbols are then demodulated using the S-QAM demodulator, whereas parity symbols are demodulated using the conventional 16-QAM demodulator, as shown in Fig.6.

For the Hybrid 2 scheme,  $R_t$  is first demodulated using the S-QAM demodulator to obtain soft bits which are then de-multiplexed into systematic and parity parts.



**Fig. 6.** Block diagram of the receiver system.

Parity soft bits are reordered back into their original positions and the systematic and reordered-back parity soft bits are sent to the LDPC decoder.

Binary LDPC decoding [1], [2], [16], depicted by the 'LDPC Decoder' block in Fig. 6, uses the Sum-Product Algorithm (SPA), which can be summarized as follows:

*Step 1*

These a-priori bit probabilities are expressed in terms of log-likelihood ratios (LLRs). For the AWGN channels, the LLR of the received noisy vector  $y$  is approximated as follows:

$$LLR = \frac{2y}{\sigma^2}, \quad (7)$$

where

$y$  is the received noisy vector,  
 $\sigma^2$  is the variance of the noisy vector.

*Step 2*

A matrix  $M$  of the same size as the parity check matrix,  $H$ , is initialized such that it contains all LLR values at all positions where there is a 1 in  $H$ . Matrices  $A$  and  $B$  are obtained from matrix  $H$  such that  $A$  stores the position of each bit node connected to each check node and  $B$  stores the position of each check node connected to each bit node.

*Step 3*

The extrinsic message is computed as follows:

$$E_{j,i} = \ln \left( \frac{1 + \prod_{i' \in B_j, i' \neq i} \tanh(M_{j,i'}/2)}{1 - \prod_{i' \in B_j, i' \neq i} \tanh(M_{j,i'}/2)} \right), \quad (8)$$

where

$M_{j,i'}$  is the element in matrix  $M$  in row  $j$  and column  $i'$ ,  
 $B_{j,i'}$  is the element in matrix  $B$  in row  $j$  and column  $i'$ .

*Step 4*

The total LLR of the  $i$ -th bit,  $L_r$  is the sum of the input a priori LLRs,  $r_i$ , and the LLRs from every check node connected to the bit as follows:

$$L_i = r_i + \sum_{j \in A_i} E_{j,i}, \quad (9)$$

where

$E_{j,i}$  is the element in matrix  $B$  in row  $j$  and column  $i$ ,  
 $r_i$  is the  $i$ <sup>th</sup> a priori LLR.

*Step 5*

The code-word is finally decoded by using the sign of the total LLR of each bit (a hard decision). Positive LLRs imply a bit decoded as a 0, while negative LLRs imply a bit decoded as a 1.

To check whether all parity-check constraints have been satisfied, the syndrome,  $s$ , is calculated as follows:

$$s = Hz^T, \quad (10)$$

where

$z^T$  is the transposed decoded LDPC code-word vector,  
 $H$  is the LDPC parity check matrix.

If the syndrome is zero, it implies that all parity check constraints are satisfied and the decoding process is stopped. Else the decoder proceeds with the following step.

*Step 6*

The messages received by the check nodes from the bit nodes are calculated as follows:

$$M_{j,i} = \sum_{j' \in A_i, j' \neq j} E_{j',i} + r_i, \quad (10)$$

where

$M_{j,i}$  is the element in matrix  $M$  in row  $j$  and column  $i$ ,  
 $E_{j',i}$  is the element in matrix  $M$  in row  $j'$  and column  $i$ ,  
 $r_i$  is the  $i$ <sup>th</sup> a priori LLR.

The decoder then proceeds with the next iteration which begins with the calculation of the extrinsic message in Step 3 by using the updated matrix  $M$ .

The decoded systematic bits are sent to the Statistical De-mapping block, whereby they are grouped into blocks of  $\log_2(M)$  consecutive bits. The inverse S-QAM transformation is then performed according to the statistical transformation lookup table (Table 2) so that the message bit-stream is recovered.

Finally, the message bit-stream is decoded using a VLC decoder to recover the alphabet stream.

### 3. SIMULATION RESULTS AND ANALYSIS

The performances of the following schemes with binary LDPC codes as well 16 and 64 QAM are compared:

- Scheme 1: Hybrid 1 scheme.
- Scheme 2: Hybrid 2 scheme.
- Scheme 3: Hybrid 1 scheme with statistical QAM mapping but without UEP.

- Scheme 4: Hybrid 2 scheme with statistical QAM mapping but without UEP.
- Scheme 5: UEP with bit reordering for both systematic and parity bits and conventional QAM.
- Scheme 6: Conventional LDPC encoding and decoding without UEP.

The simulations were performed on MATLAB® using the IEEE 802.11n LDPC matrices and the simulation parameters are as follows:

- Number of decoding iterations,  $T = 20$ .
- Channel Model: Complex AWGN.
- Modulation: 16 QAM and 64 QAM.
- Code-rates:  $R = 1/2, 2/3$  and  $3/4$ .
- Code-length,  $G = 648$ .

In all schemes, a total of 476191 alphabets (around 1 million bits) were packetised and transmitted.

### 3.1. SIMULATION RESULTS WITH 16-QAM

The graphs of BER against  $E_b/N_0$  for the six schemes using code-rates  $1/2, 2/3$  and  $3/4$  with 16-QAM are shown in Figures 7-9. It is observed that both proposed hybrid schemes, i.e. Schemes 1 and 2, provide the highest  $E_b/N_0$  gains compared to Scheme 6 with every tested code-rates at BER values less than  $10^{-2}$ . The gains that the Hybrid 1 scheme (Scheme 1) gives over Scheme 6 are 0.23 dB, 0.34 dB and 0.68 dB for code-rates  $1/2, 2/3$  and  $3/4$ , respectively, in the range  $10^{-3} \leq \text{BER} \leq 10^{-5}$ . In the same BER range, the gains that the Hybrid 2 scheme (Scheme 2) gives compared to Scheme 6 are 0.3 dB, 0.68 dB and 0.62 dB for code-rates  $1/2, 2/3$  and  $3/4$ , respectively. Hence the Hybrid 2 scheme outperforms the Hybrid 1 scheme by 0.1 dB with the code-rate  $1/2$  and 0.3 dB with the code-rate  $2/3$ , whereas when using the code-rate  $3/4$  Scheme 1 gives a gain of 0.06 dB compared to Scheme 2. It is also observed that in the region  $\text{BER} \geq 10^{-2}$ , the proposed hybrid schemes do not provide gains compared to Schemes 5 and 6.

Scheme 5 provides  $E_b/N_0$  gains mainly at the BER greater than  $10^{-2}$  with the maximum gain of 2 dB, obtained when using the LDPC code-rate  $1/2$  and the minimum gain of 0.4 dB obtained when using the LDPC code-rate  $3/4$ . However, at BER values lower than  $10^{-3}$ , the scheme provides a gain of 0.17 dB with the LDPC code-rate  $3/4$  and almost no gain with the LDPC code-rate  $1/2$ . The difference in gains observed in Scheme 5 when using different code-rates is mainly due to the fact that the bit nodes have more degree variations in the tested IEEE 802.11n LDPC parity check matrix with the code-rate  $3/4$  than the one with the code-rate  $1/2$ .

Similarly, the gain in  $E_b/N_0$  that Scheme 1 provides compared to Scheme 3 and that Scheme 2 provides compared to Scheme 4 is dependent on the variation in parity bit node degrees. In most cases, Scheme 1 provides a gain of 0.1 dB compared to Scheme 3 and

Scheme 2 provides the same gain compared to Scheme 4. Hence the hybrid schemes benefit from performance gains due to both the S-QAM and UEP characteristics.

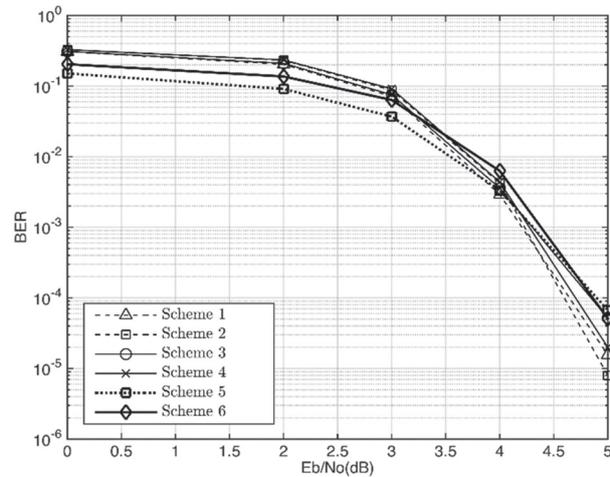


Fig. 7. Graph of  $E_b/N_0$  against BER using 16-QAM with  $R=1/2$ .

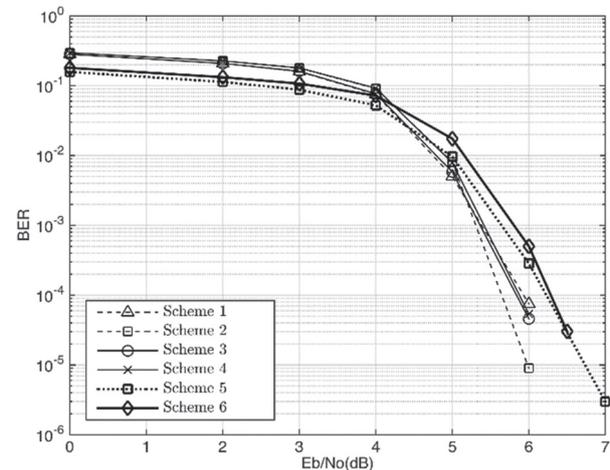


Fig. 8. Graph of  $E_b/N_0$  against BER using 16-QAM with  $R=2/3$ .

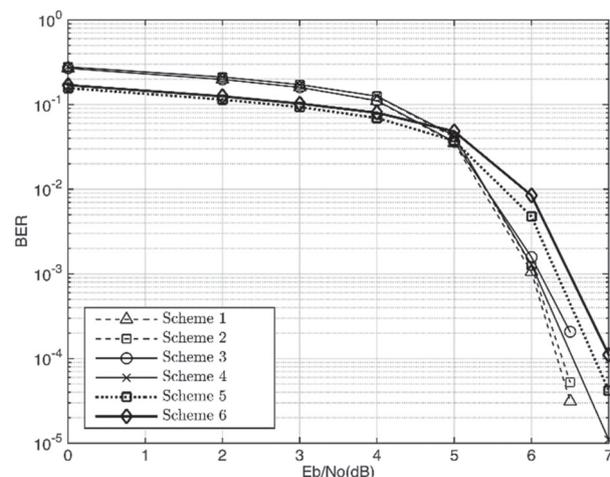
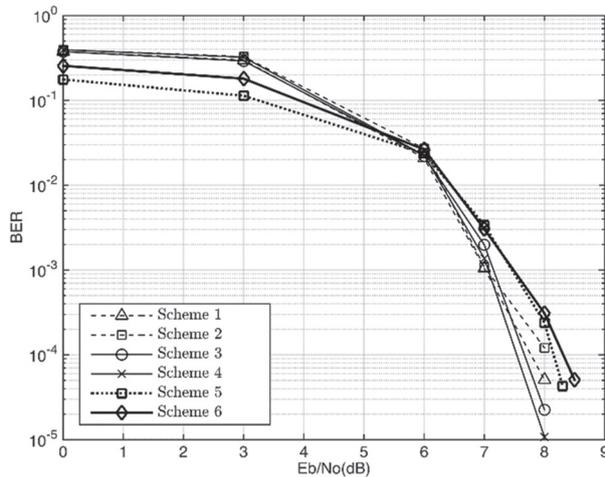


Fig. 9. Graph of  $E_b/N_0$  against BER using 16-QAM with  $R=3/4$ .

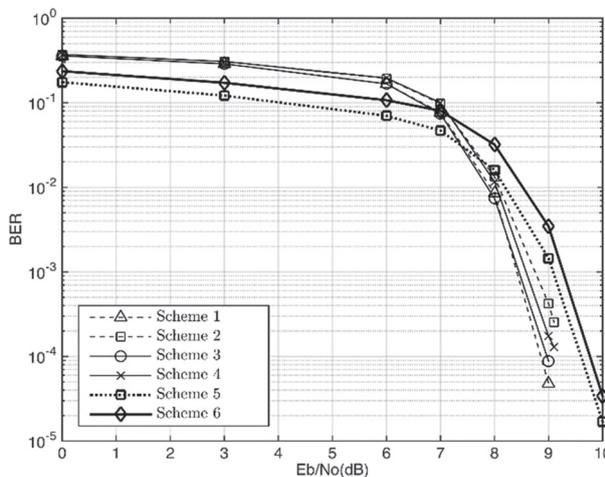
### 3.2. SIMULATION RESULTS WITH 64-QAM

The graphs of BER against  $E_b/N_0$  for the six schemes using code-rates 1/2, 2/3 and 3/4 with 64-QAM are shown in Figures 10-12. It can be observed that Scheme 1 provides gains of 0.5 dB, 0.9 dB and 0.78 dB compared to Scheme 6 when using code-rates 1/2, 2/3 and 3/4, respectively, in the range  $10^{-3} \leq \text{BER} \leq 10^{-4}$ . In the same BER range, the gains that Scheme 2 gives compared to Scheme 6 are 0.3 dB, 0.46 dB and 0.91 dB. Hence for 64-QAM, Scheme 1 outperforms Scheme 2 by 0.3 dB with the code-rate 1/2 and by 0.58 dB except when using the code-rate 3/4, whereby Scheme 2 gives a gain of 0.13 dB compared to Scheme 1.

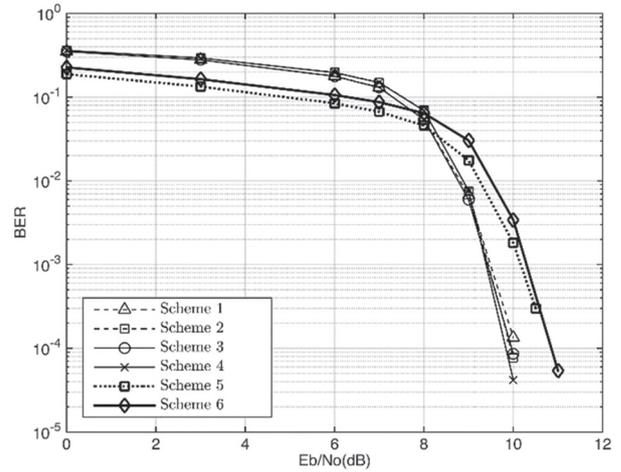
It is also observed that with 64-QAM, the Hybrid scheme does not provide a significant gain compared to the S-QAM scheme, which does not use prioritised bit ordering. This may be because with 64-QAM there are three priority levels. Moreover, the proposed hybrid schemes also do not provide gain compared to Schemes 5 and 6 in the region  $\text{BER} \geq 10^{-2}$ , as observed with 16-QAM.



**Fig.10.** Graph of  $E_b/N_0$  against BER using 64-QAM with  $R=1/2$ .



**Fig.11.** Graph of  $E_b/N_0$  against BER using 64-QAM with  $R=2/3$ .



**Fig.12.** Graph of  $E_b/N_0$  against BER using 64-QAM with  $R=3/4$ .

When comparing the Hybrid scheme performances with 64-QAM and with 16-QAM, it is observed that the schemes give higher gains when using 64-QAM. This can be explained by the fact that the calculated separation distances between the constellation points,  $d_s$ , for 64-QAM were larger than those calculated for 16-QAM, as shown in Table 3 and 4.

Moreover, with 16-QAM, the Hybrid 2 scheme gives higher gains than the Hybrid 1 scheme, whereas with 64-QAM, the Hybrid 1 scheme outperforms the Hybrid 2 scheme.

The proposed schemes work best with the code-rate 3/4 and 2/3 LDPC codes because the bit nodes corresponding to the parity bits had more degree variations than in the code-rate 1/2 LDPC case. The performance of the proposed schemes can be improved even with the code-rate 1/2 LDPC case by using an LDPC parity check matrix, whereby the bit nodes corresponding to the parity have more degree variations.

It is also clearly observed that in all simulation results obtained, both hybrid schemes proposed are outperformed by Schemes 5 and 6 in the range  $\text{BER} \geq 10^{-2}$ , but they effectively outperform Schemes 5 and 6 over the useful BER range,  $\text{BER} \leq 10^{-2}$ .

### 4. CONCLUSION

This paper proposed two versions of a hybrid UEP scheme for the IEEE 802.11n LDPC codes. The scheme involved mapping of the QAM symbols with the highest probability occurrence onto the low power regions of the QAM constellation so as to decrease the overall transmission power, hence allowing the constellation points to be further separated so as to reduce the BER of the system. Additionally, a bit reordering scheme is incorporated prior to the QAM modulator with a view to providing better protection to the higher priority parity bits. Simulations were performed using binary LDPC codes with 16 and 64 QAM. With 16-QAM, the proposed schemes provided a maximum gain of 0.68 dB in  $E_b/N_0$  for BERs lower than  $10^{-2}$  and with 64-QAM, a maximum gain of 0.91 dB in  $E_b/N_0$  was

obtained. The contribution of the paper is a new hybrid UEP scheme for LDPC codes with QAM which can provide gains of up to 0.91 dB in Eb/No as compared with other UEP schemes for a range of BER values. However, it was complex to tune the parameters of the hybrid schemes to obtain a gain in the BER > 10<sup>-2</sup> region and so, for future work, the proposed hybrid schemes can be further improved to obtain gains for that region. Another possible future work can be to incorporate other UEP schemes into this framework so as to obtain even further gains. Moreover, the hybrid UEP scheme can be implemented with Non-Binary LDPC codes with decoding algorithms such as the Belief-Propagation Algorithm [18], [19].

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